



armStone™ A5 PicoITX with Vybrid Dual-Core CPU by Freescale

F&S Elektronik Systeme GmbH from Stuttgart expands its successful PicoITX product family for another module – the new armStone™ A5. The first version of armStone™ A5 with Vybrid Single-Core CPU **ARM® Cortex™-A5 and Linux was introduced in 2013. Now there are further versions available:** Vybrid Dual-Core CPU (Asymmetric Multiprocessing) **ARM® Cortex™-A5 & Cortex™-M4 with Linux and Windows Embedded Compact 2013.** The Vybrid CPU's special combination of **ARM® Cortex™-A5 with a Cortex™-M4 Core on the same chip enables completely new application areas, where graphics and real-time are integrated in one CPU. Further highlights are 10 years of availability and the CPU's extended temperature range.**

armStone™ A5 offers up to 512MB RAM, 1GByte Flash and standard connectors for 2x Ethernet, USB Host, USB Device and Audio Out. Digital RGB (up to SVGA) as well as LVDS (up to XGA) are offered for display connection. A 4-wire resistive touch, as well as a PCAP-Touch (via I²C), are offered for touch connection. Other interfaces are 2x CAN, I²C, SPI, 3x RS232, GPIO and Audio LIN IN/OUT/MIC. The board is supplied with 5V or 8-14V, the power consumption is 3W (typ.). In case the performance should not be sufficient, we also offer a PicoITX Board with i.MX 6 CPU. Possible applications are displaying, controlling and communication devices with a display from 3.5" up to 15" in industrial and medical engineering. As an operating medium one can use a 4-wire resistive touch panel, as well as a PCAP touch panel (with additional glass plate also). This enables a highly diversified application field. The optimal price-performance-ratio makes armStone™ A5 suitable for annual quantities of more than 1.000 pieces.

Find more information at www.fs-net.de

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