# Hardware Documentation

## *for JTAG Debug Adapter*

Version 1.0
2014-12-02
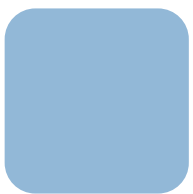
# History

| Date | V | Platform | A,M,R | Chapter | Description | Au |
|------|---|----------|-------|---------|-------------|-----|
| 01.12.2014 | 0.1 | All | A | - | Build the document | MW |
| 03.12.2014 | 1.0 | All | M | * | Changed to new company CI | JG |
| 05.12.2014 | 1.0 | All | A | 5 | Added Chapter 5 | JG |
| V | Version | | | | | |
| A,M,R | Added, Modified, Removed | | | | | |
| Au | Author | | | | | |

# About This Document

This document describes how to use the F&S JTAG Debug Adapter with mechanical and electrical information. The latest version of this document can be found at:

http://www.fs-net.de.

# Table of Contents

# 1 Mechanical dimension



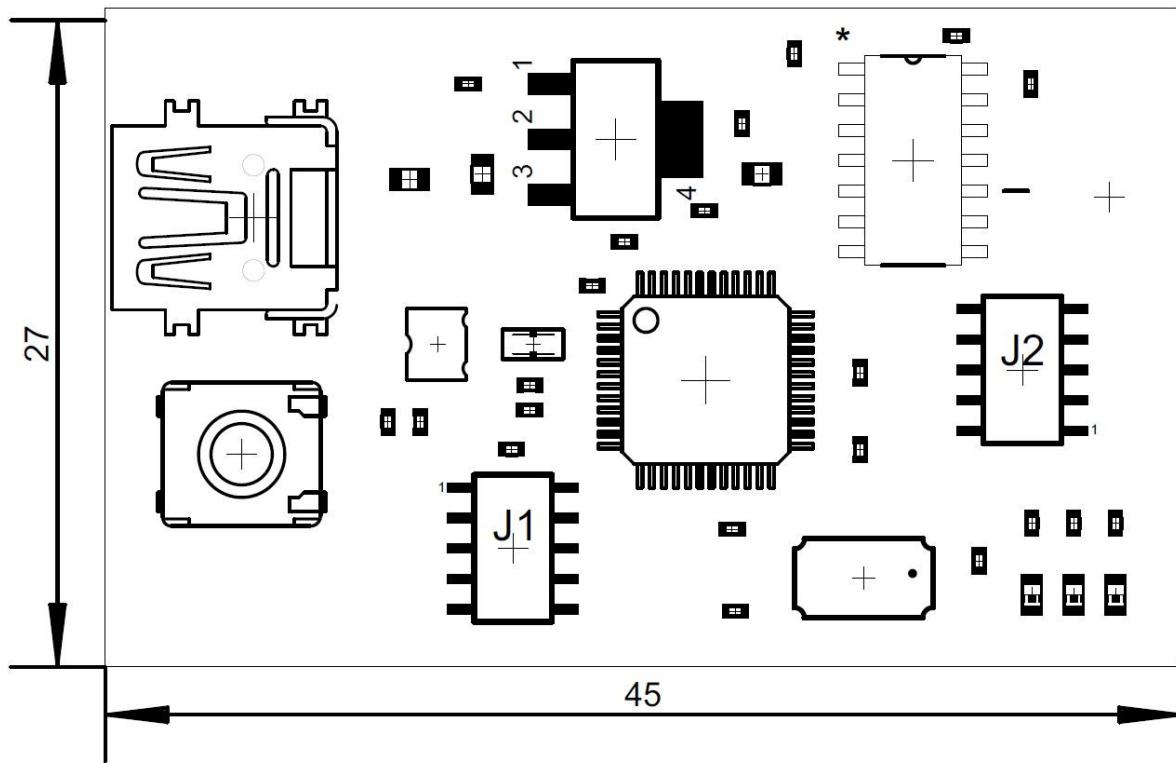*Figure 1: mechanical dimension*

## 1.1 Module

PCB size:                                27mm x 45mm
PCB thickness:                           1.6 ± 0.1mm
Height of parts on the top side:         tbd
Height of parts on the bottom side:      tbd
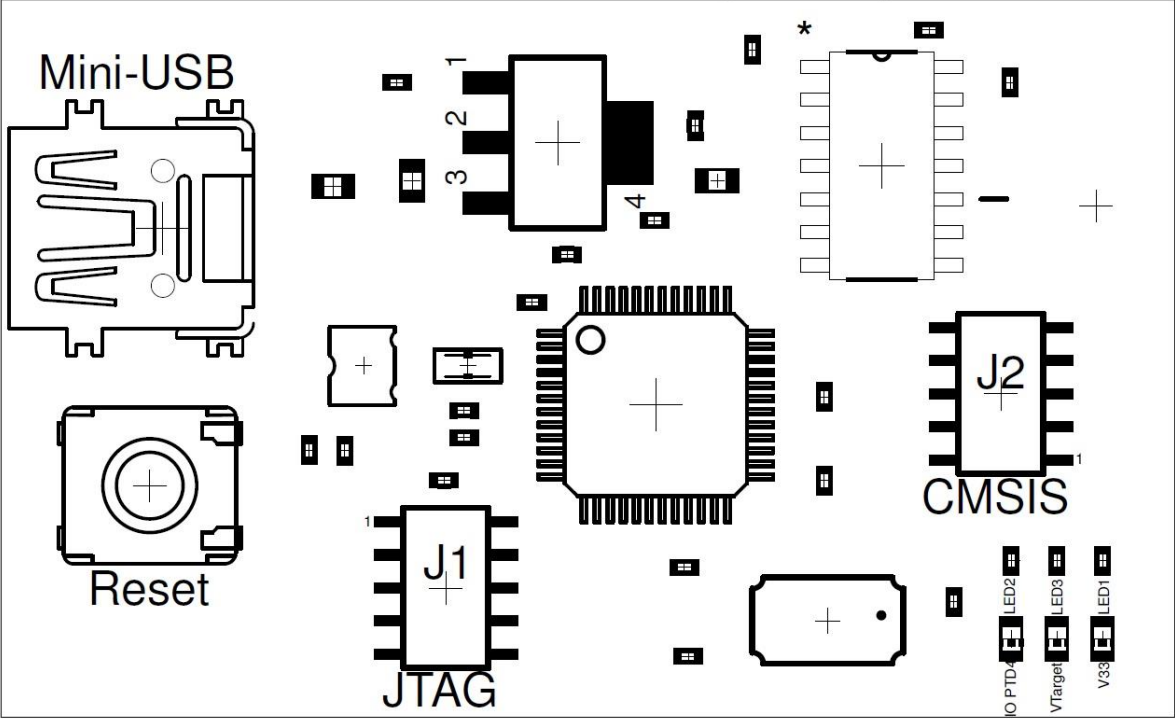Weight:                                  tbd

# 2 Connector layout



*Figure 2: Connector Layout Top*

# 3    Connect the Adapter
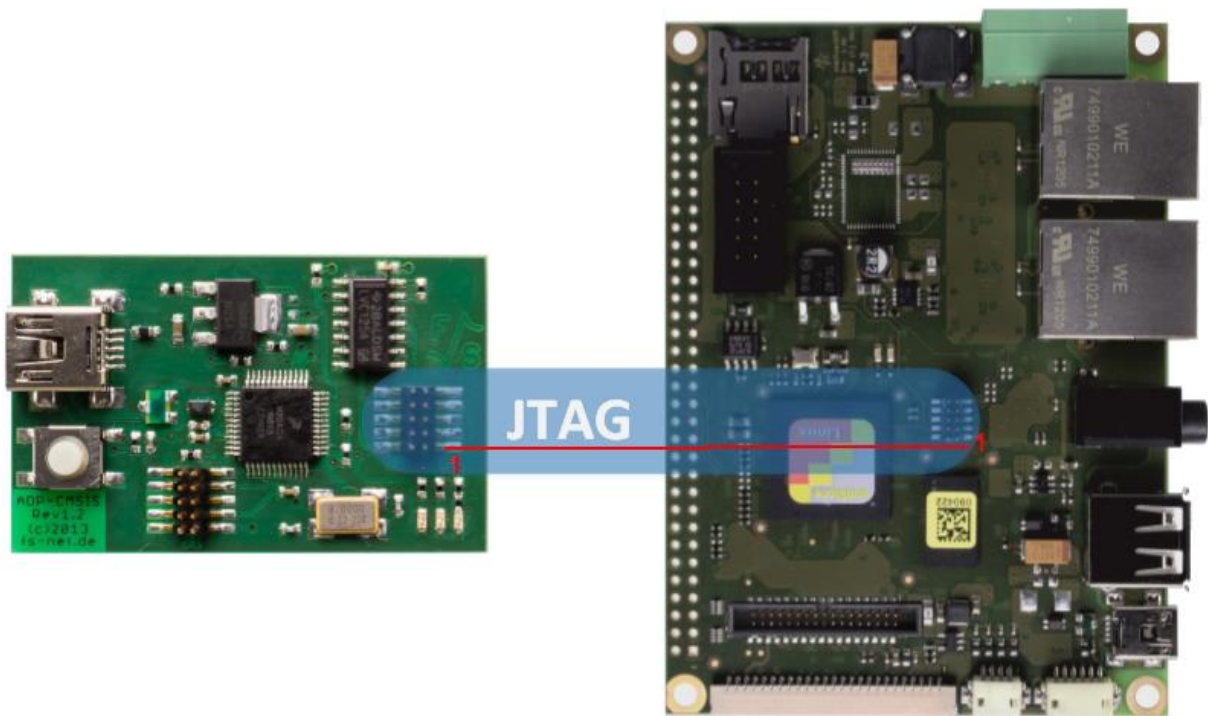


*Figure 3: How to connect the Board*

**Connect pin 1 of the adapter to pin 1 of the board!**

# 4   Interface and signal description

## 4.1  J2 CMSIS

| J1 | |
|-----|------------------|
| Pin | Function |
| 1 | 3.3V Target |
| 2 | SWD_DIO |
| 3 | Ground |
| 4 | SWD_CLk |
| 5 | Ground |
| 6 | Not connected |
| 7 | Not connected |
| 8 | Not connected |
| 9 | Not connected |
| 10 | SWD_RST |

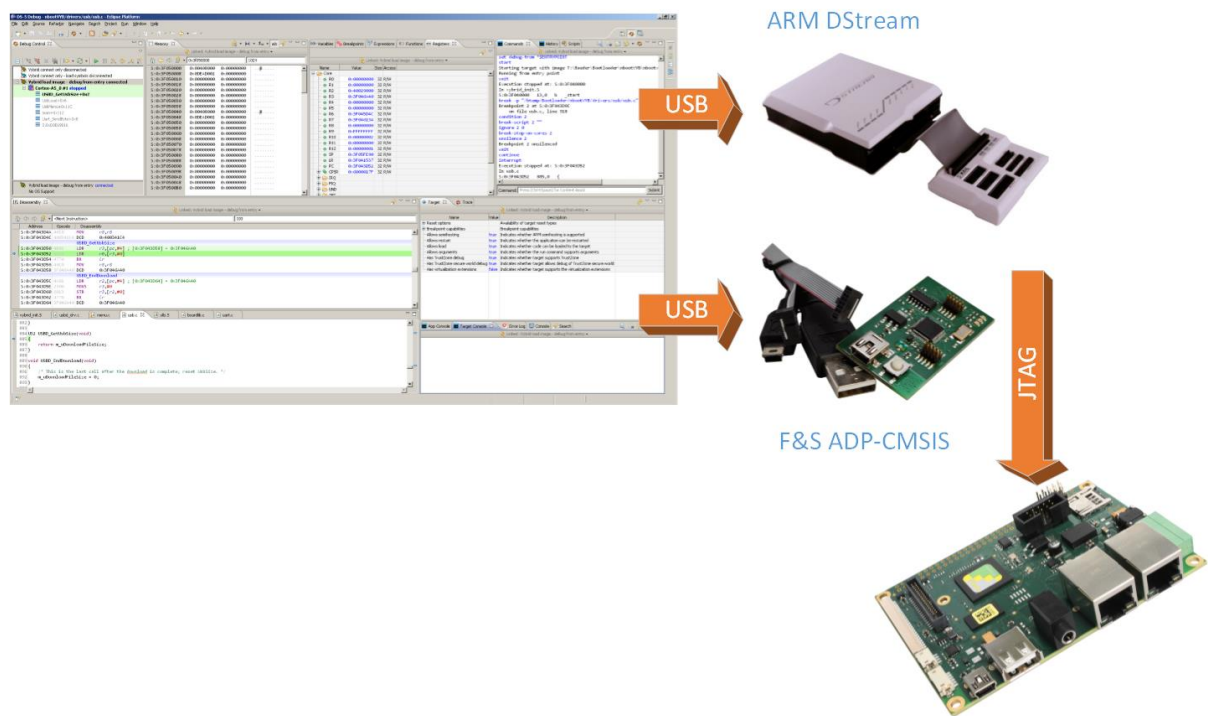# 5 Freescale Vybrid System development with ARM DS-5



*Figure 4: Needed tools and connections*

## 5.1 Freescale Vybrid MQX: Ping Pong ARM DS-5 Demo

Needed Tools:
- ARM DS-5 ver. 5.15.0
- Freescale MQX 4.0.2
- F&S ADP-CMSIS

Install DS-5 30-day-evaluation (available on www.arm.com) and MQX DS-5 Add-On (*<mqx>*/tools/ds5/ds5_update_site.zip).

Import MQX into ARM DS-5 (File → Import → Other → Import Working Sets *<mqx>*/config/<board>/ds5/*.wsd

Import MCC library and demo (File → Import → General → Existing…)

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**Note:**

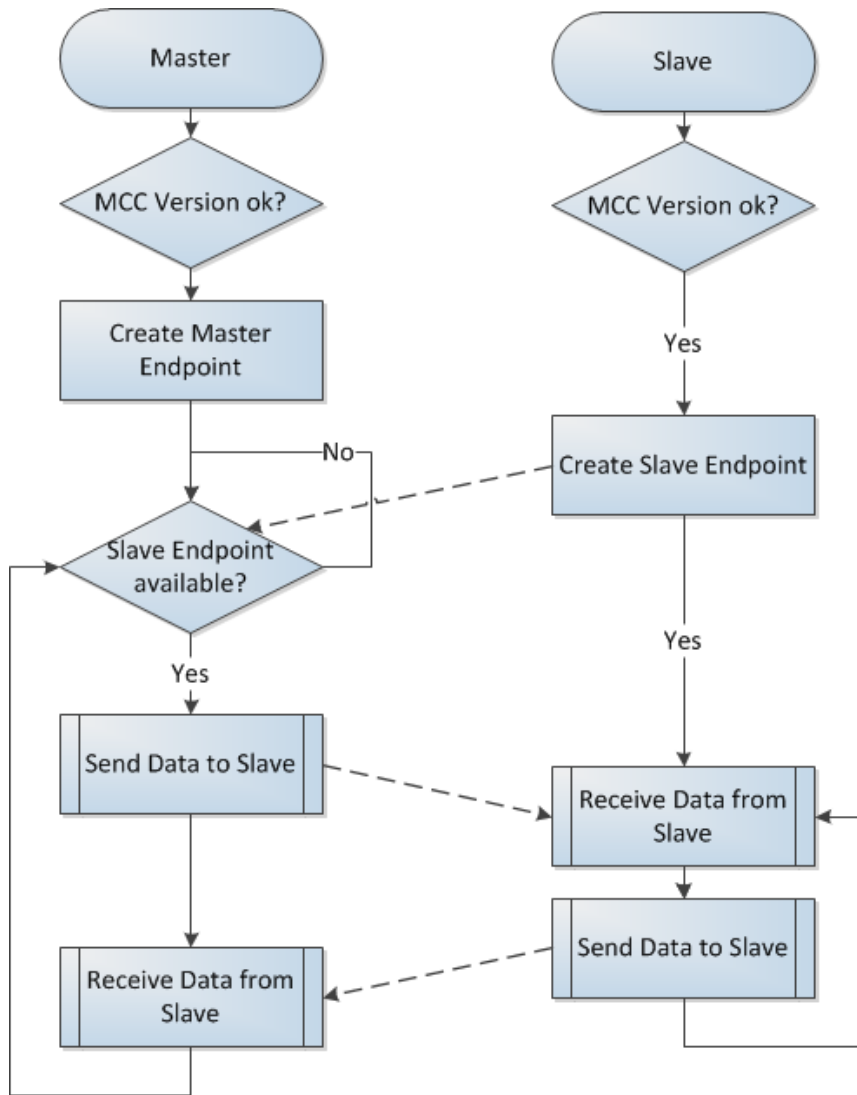Do not copy projects in workspace!

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*Figure 5: Ping Pong ARM DS-5 demo*

# 6  Appendix

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# Figures